

Con-X Reflection Gratings: Process Development Updates

Ralf K. Heilmann, Chih-Hao Chang, Yanxia Sun, Carl G. Chen,
Craig R. Forest, Paul T. Konkola, Chulmin Joo, Juan Montoya,
Mireille Akilian, Jenny You, Ed Murphy, Robert Fleming,
and Mark L. Schattenburg

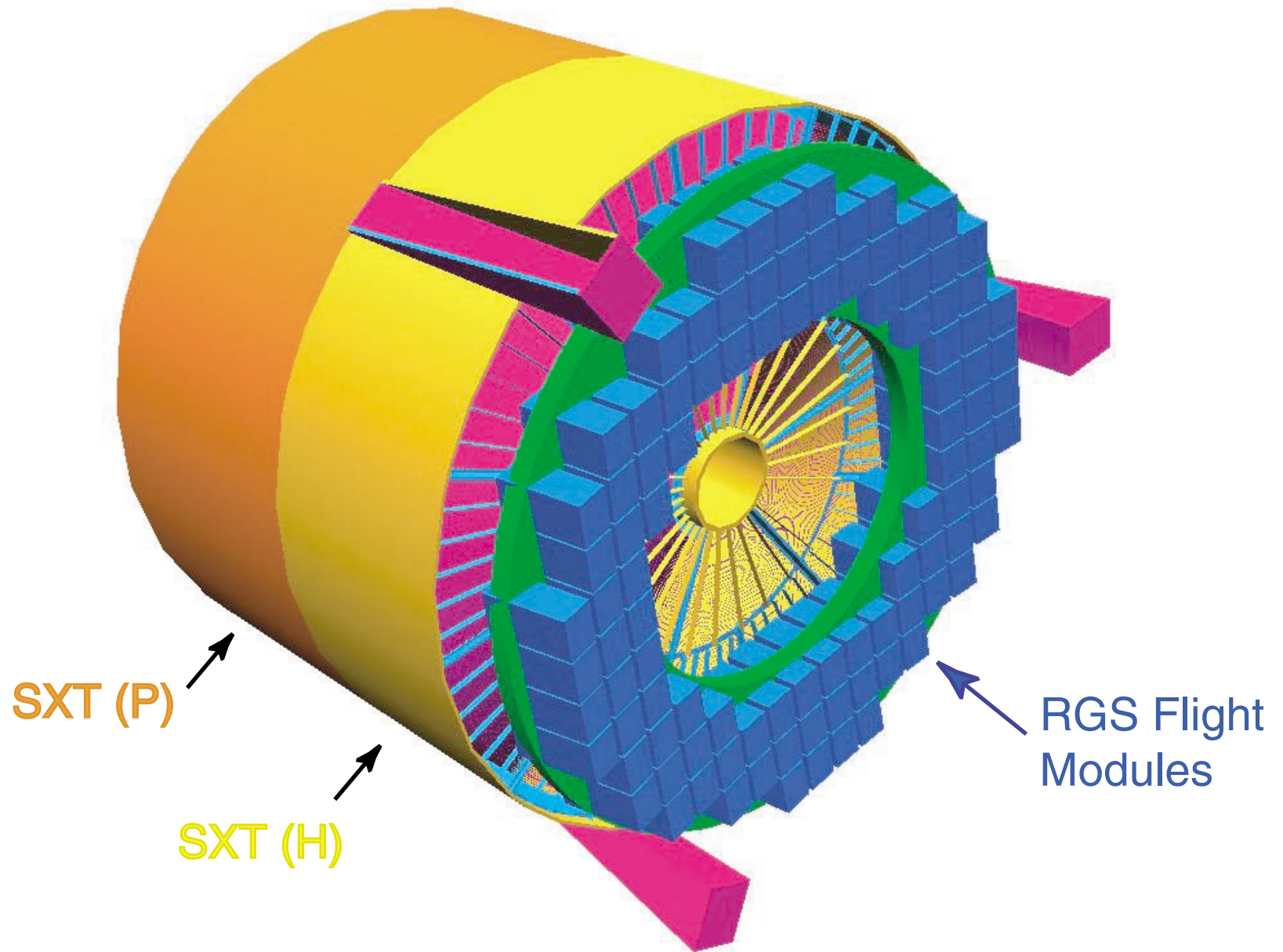
*Space Nanotechnology Lab, Center for Space Research
Massachusetts Institute of Technology*

Constellation-X Facility Science Team Meeting
Columbia University, New York
May 7-8, 2003

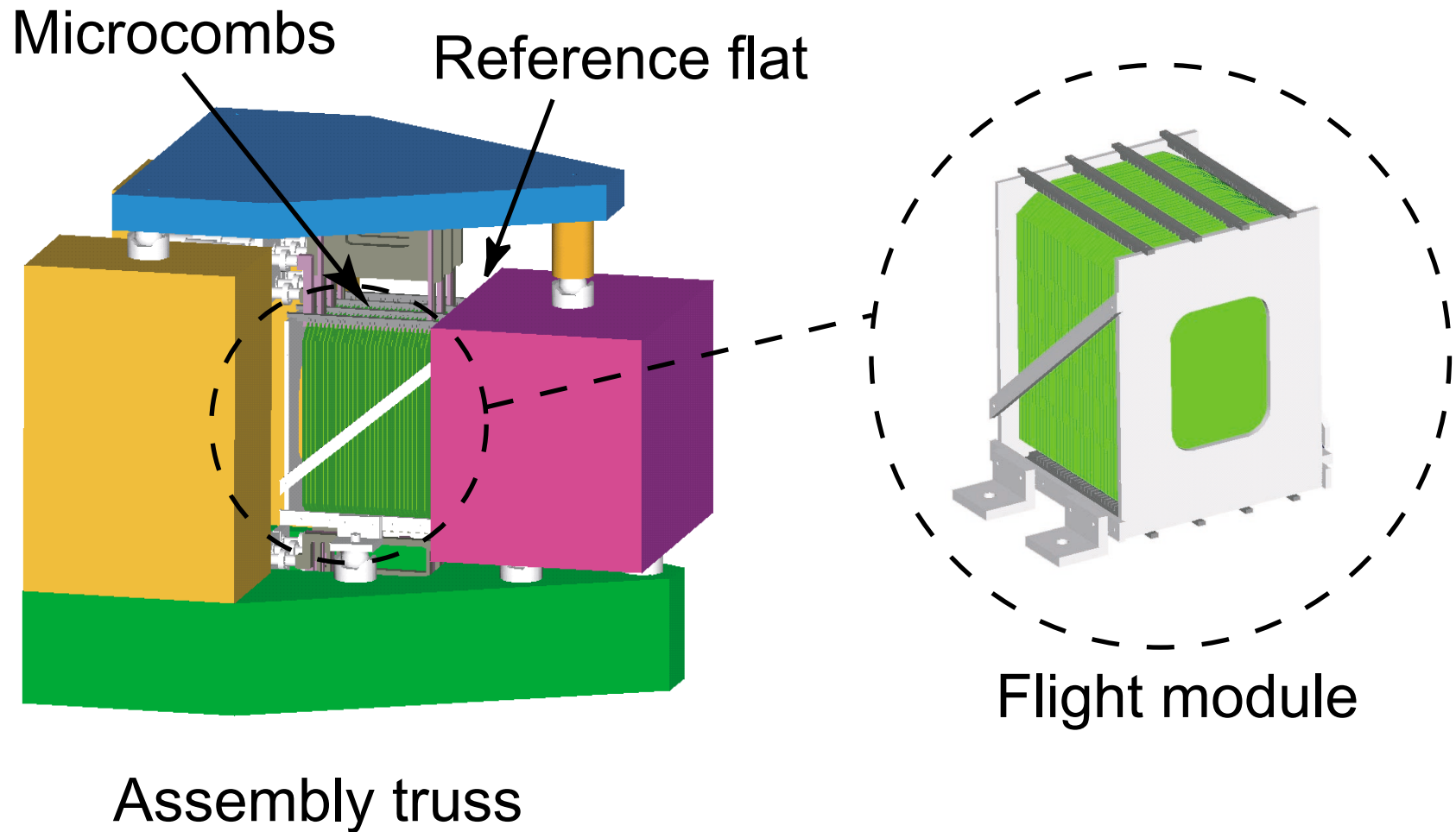
Results:

- Improved Microcomb Fabrication Process
- Patterned Large Area Gratings (300 cm Ø)
- Developed Blazed Gratings
for Off-Plane Geometry

Constellation-X Optics Concept

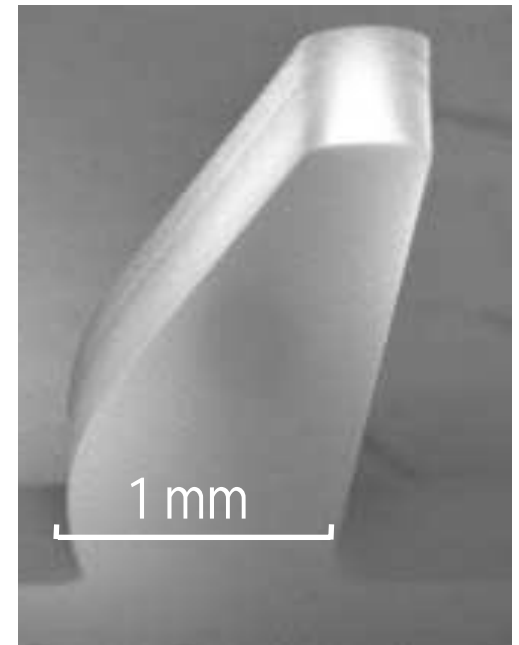
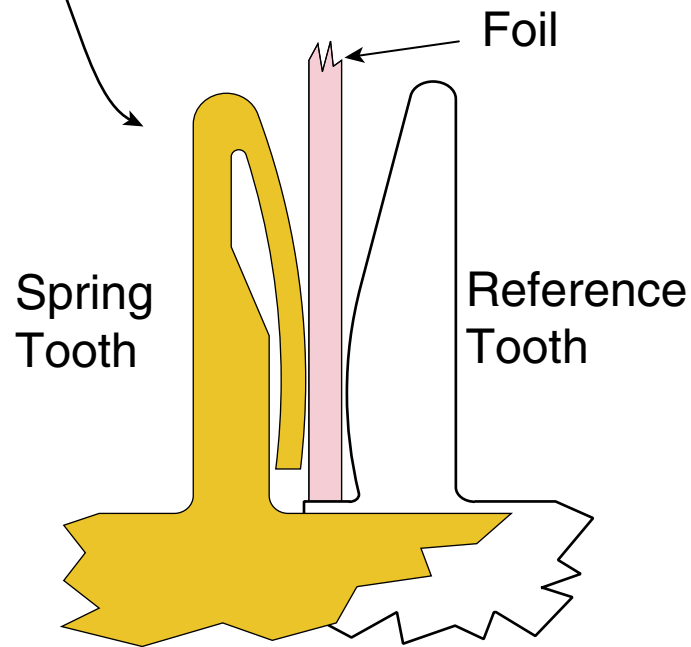
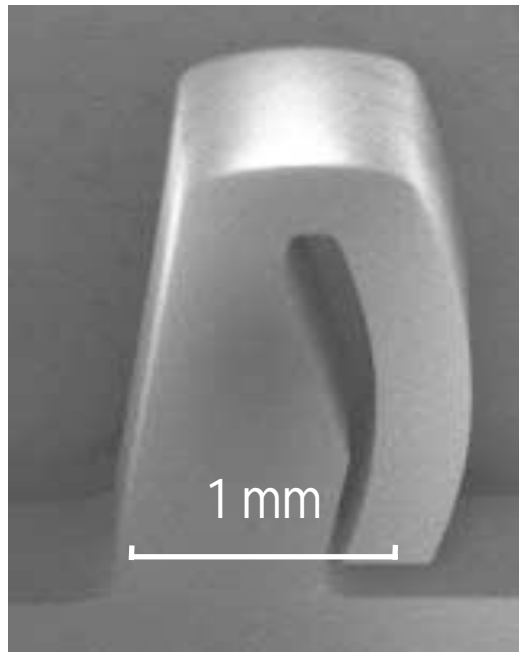
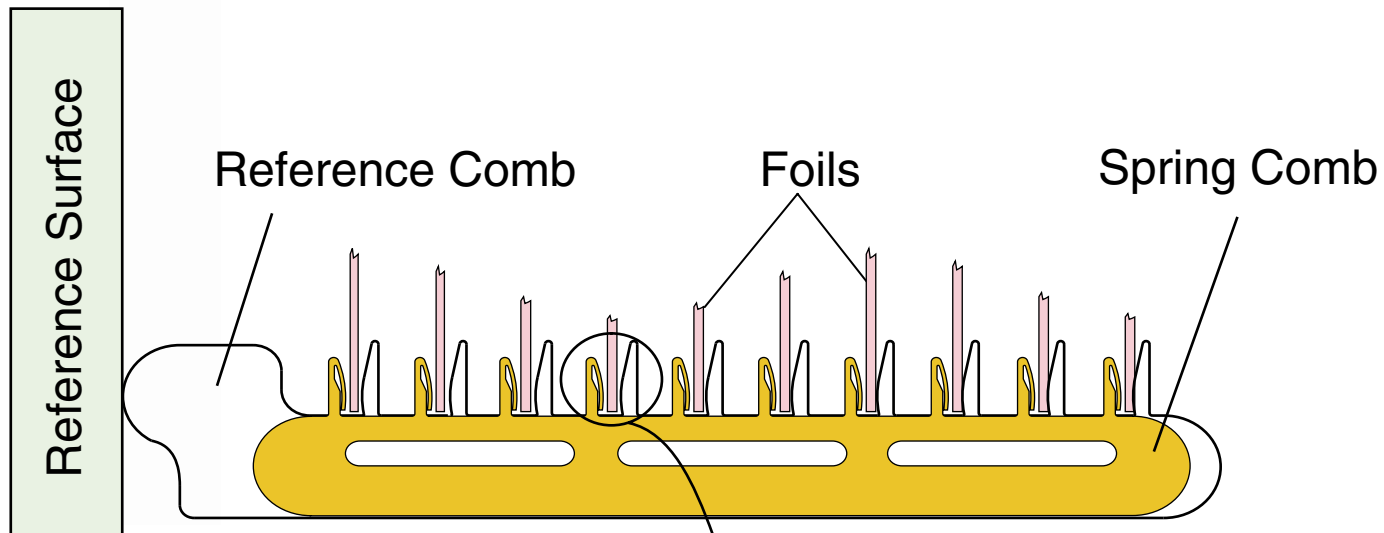


Modular Assembly



Currently $\sim 1/3$ micron (< 1 arcsec) positioning repeatability

Silicon Microcombs Establish Accurate Metrology Frame



Microcomb Fabrication Challenges: Process Uniformity and Repeatability

Solution: New BOX Wafer Double-sided Etching Process

(a) BOX silicon wafer, device layer 100(1) μm ;
handle layer 350(5) μm ; BOX 2 μm



(b) Pattern Photoresist (PR) on the device layer.



(c) Deep Reactive Ion Etch (DRIE) device layer.



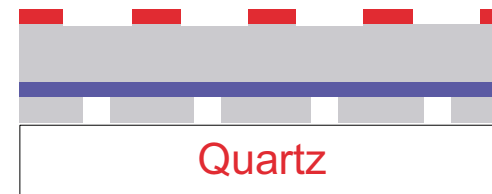
(d) Clean wafer.



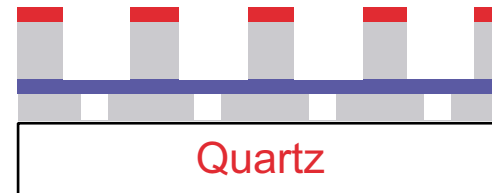
(e) Pattern PR on the handle layer.



(f) Attach wafer to quartz handle-wafer.



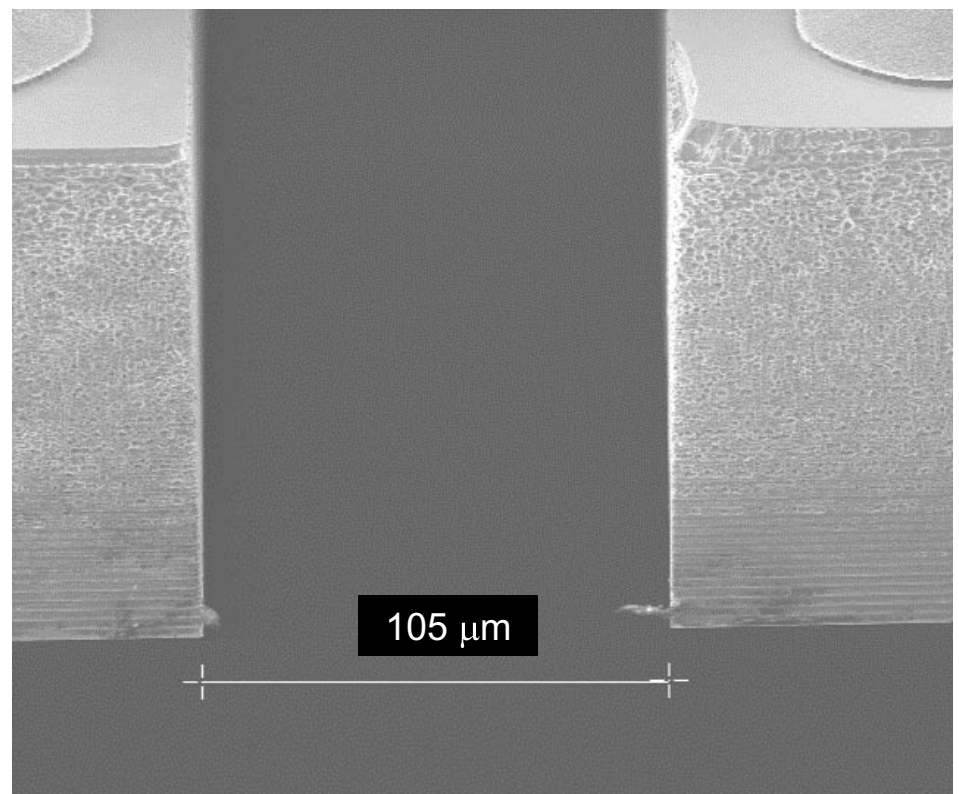
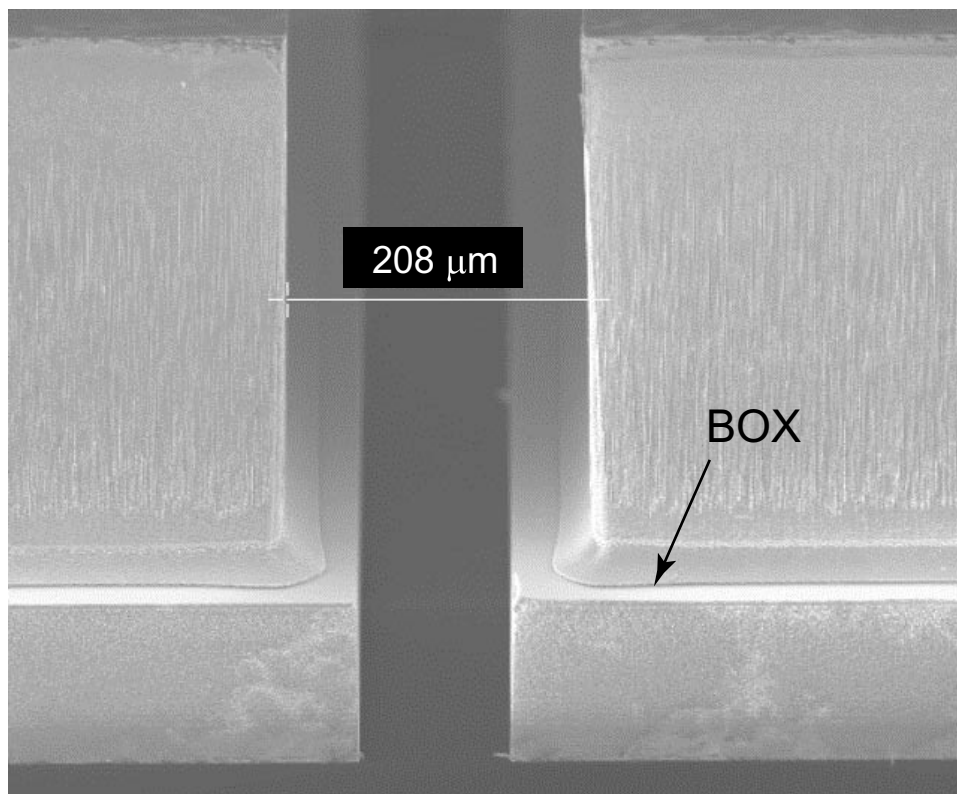
(g) DRIE handle layer
(100 min plus 10 minutes over-etching).



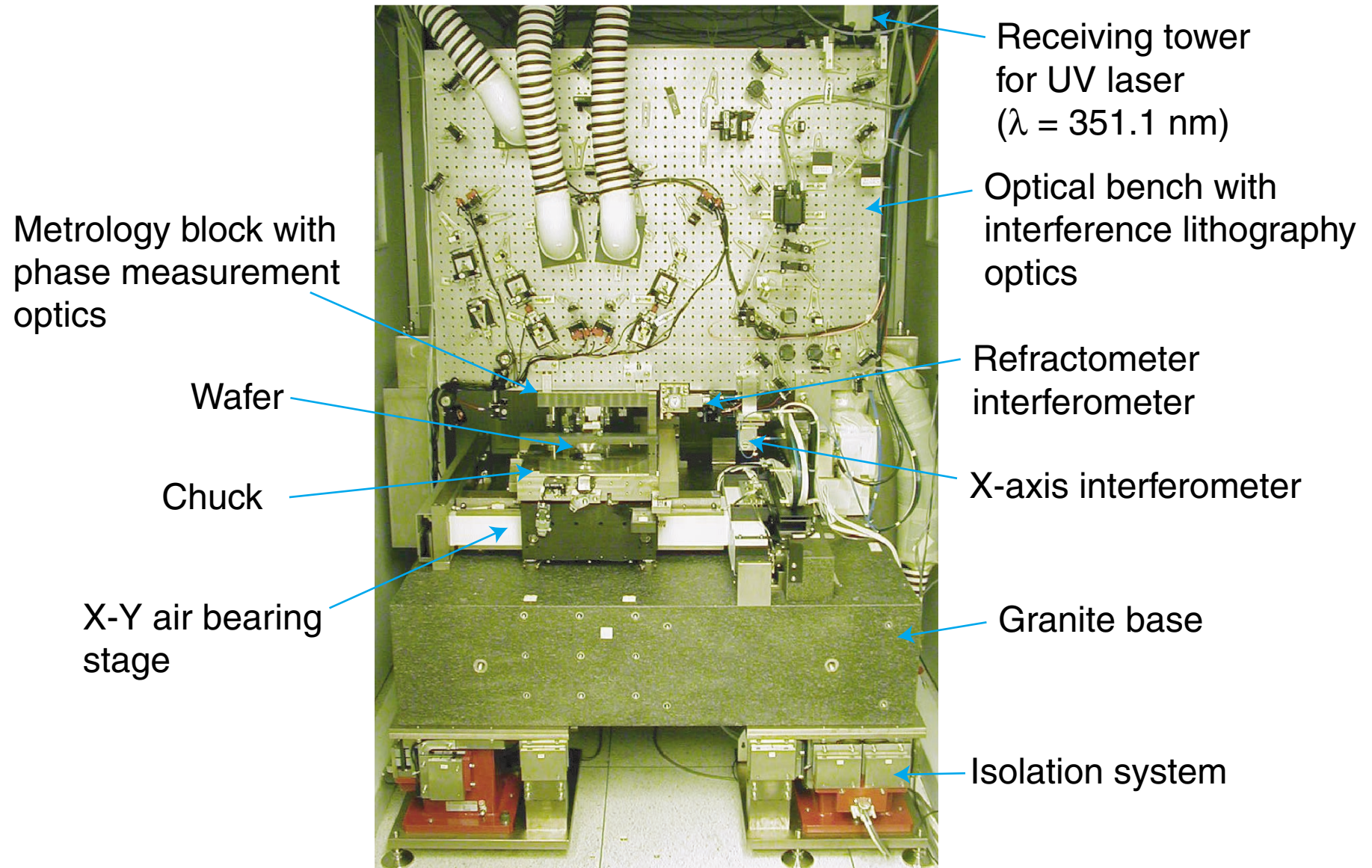
(h) Dismount wafer, BOE to strip the BOX layer, clean the wafer.



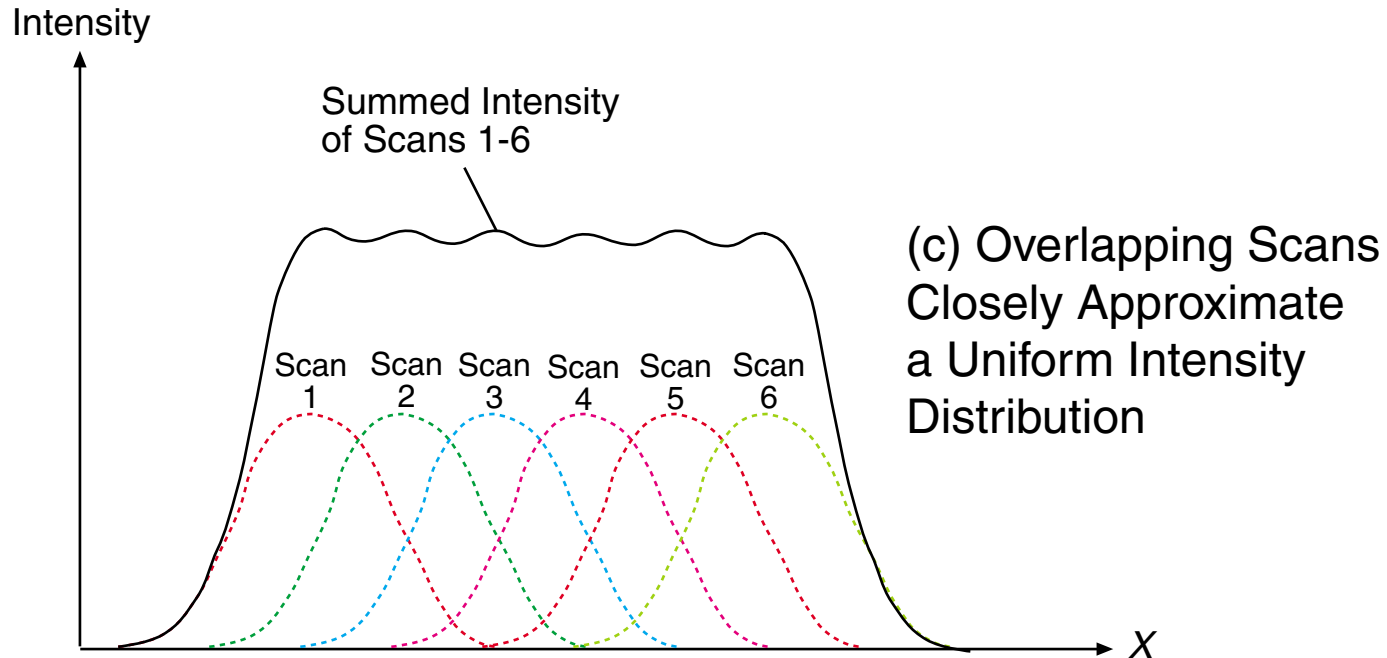
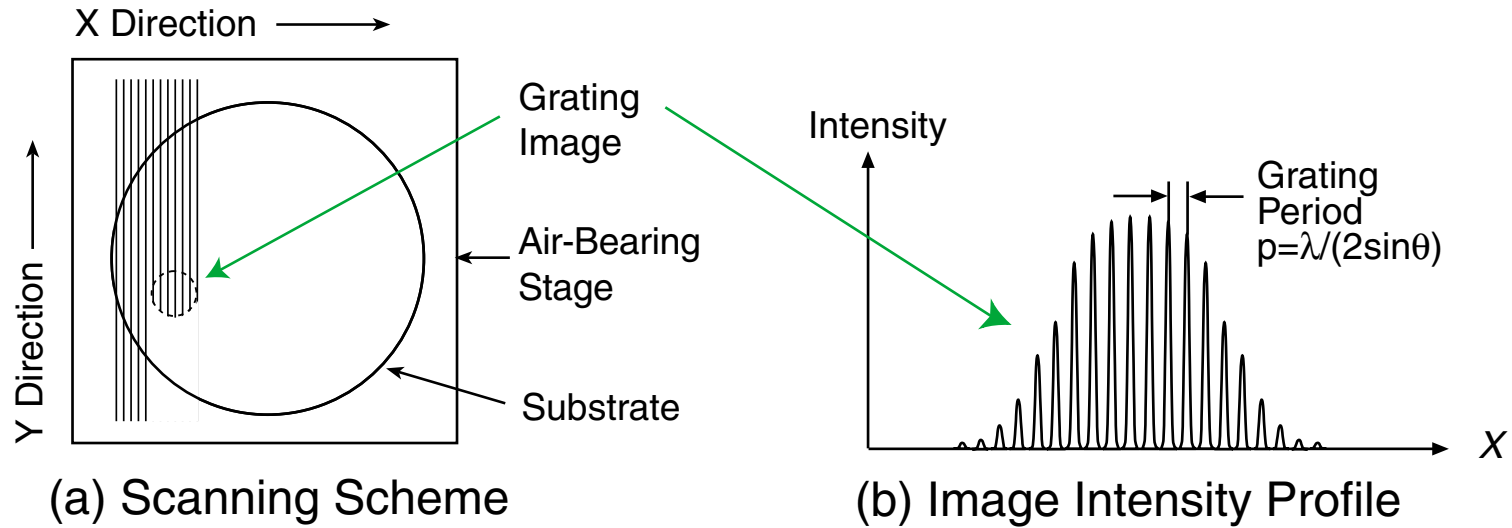
SEM Images of BOX Double-sided Etching Results



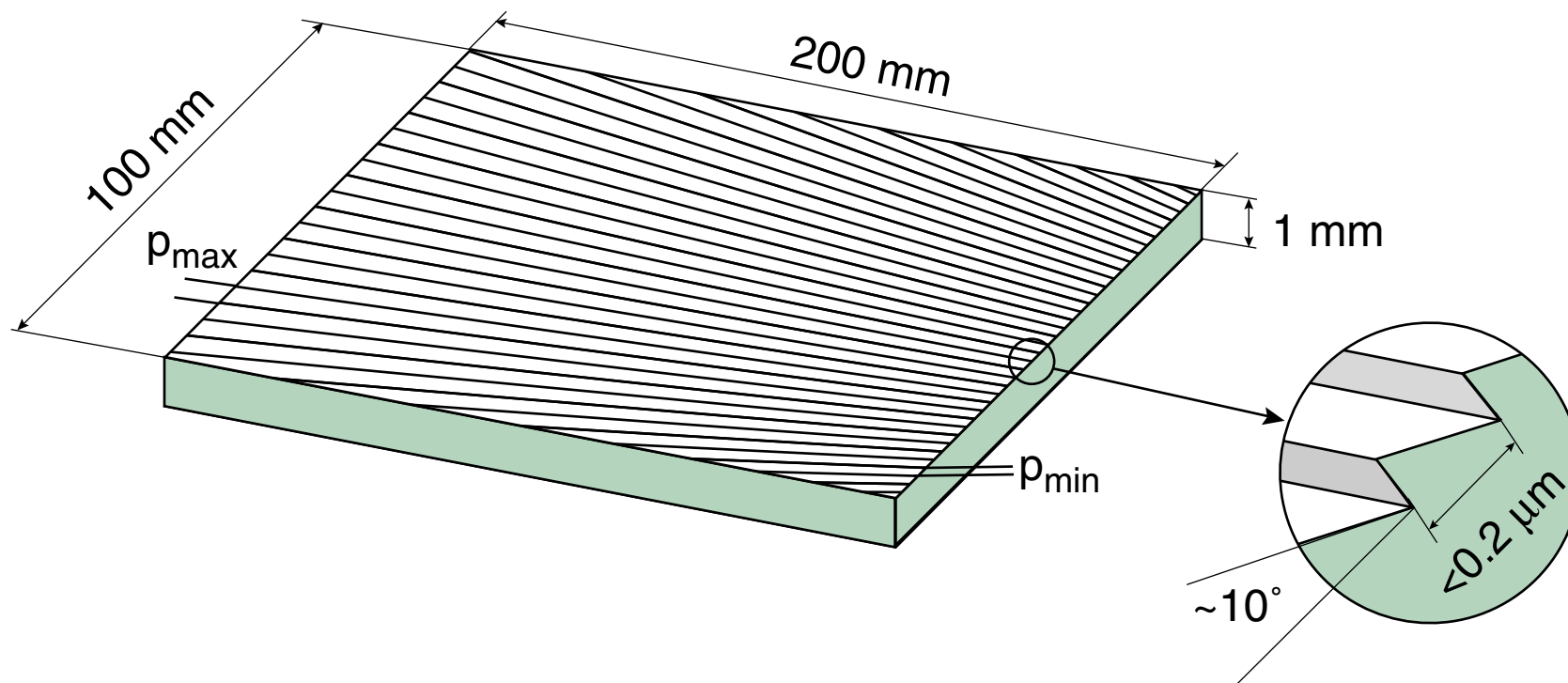
The Nanoruler - A Scanning Beam Interference Lithography Tool



Grating Scanning Method



X-ray Reflection Grating Geometry (Off-Plane Diffraction)

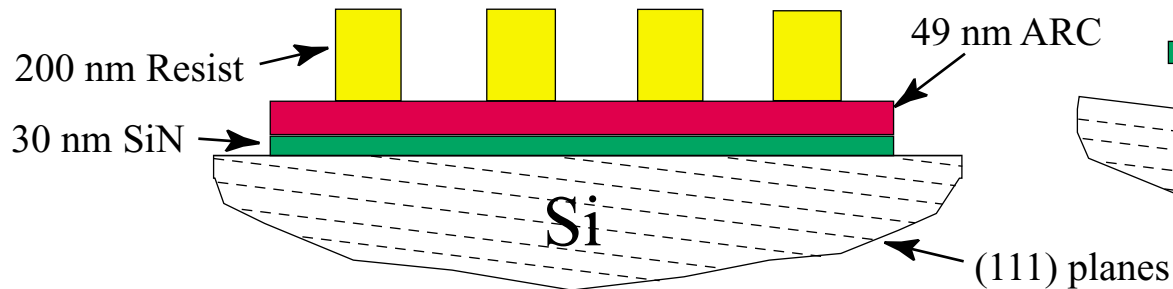


$p_{\text{ave}} < 0.2 \mu\text{m}$
Chirp $\Delta p/p \sim 2\%$
Blaze $\sim 10^\circ$

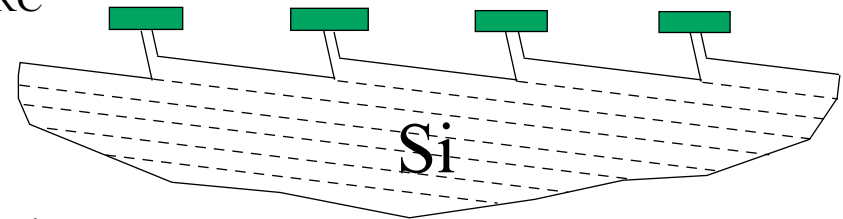
Flatness $< 1.0 \mu\text{m}$
Roughness $< 0.5 \text{ nm}$

Super-Smooth Blazed Reflection Gratings From Miscut Silicon

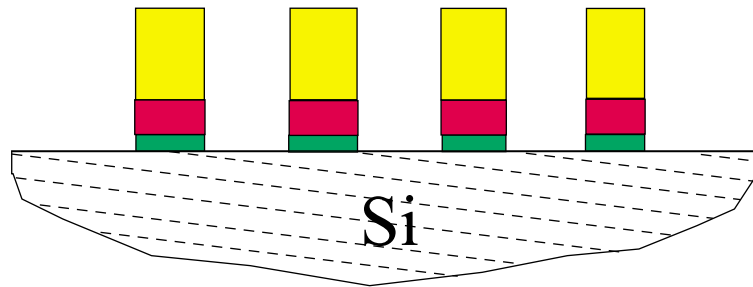
1. Coat with bilevel resist and pattern gratings.



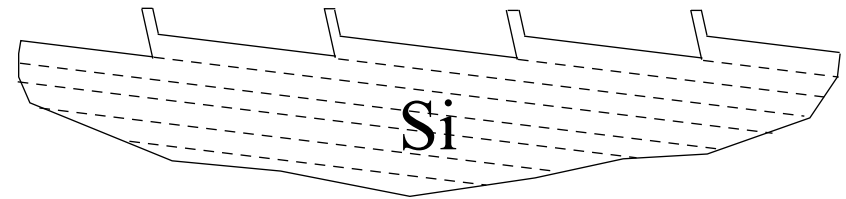
4. Anisotropic KOH etch.



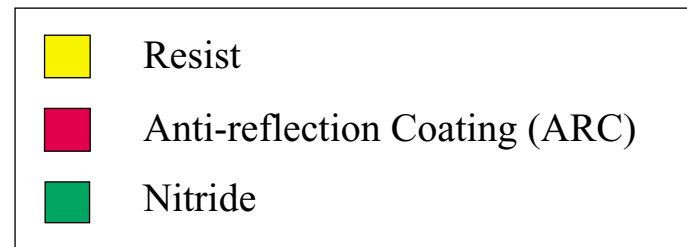
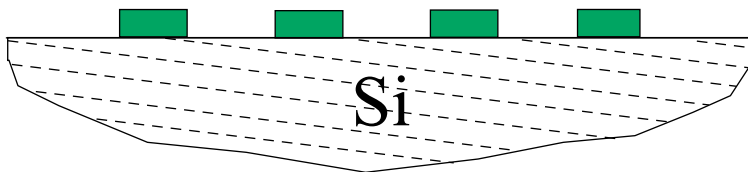
2. Plasma etch ARC and nitride.



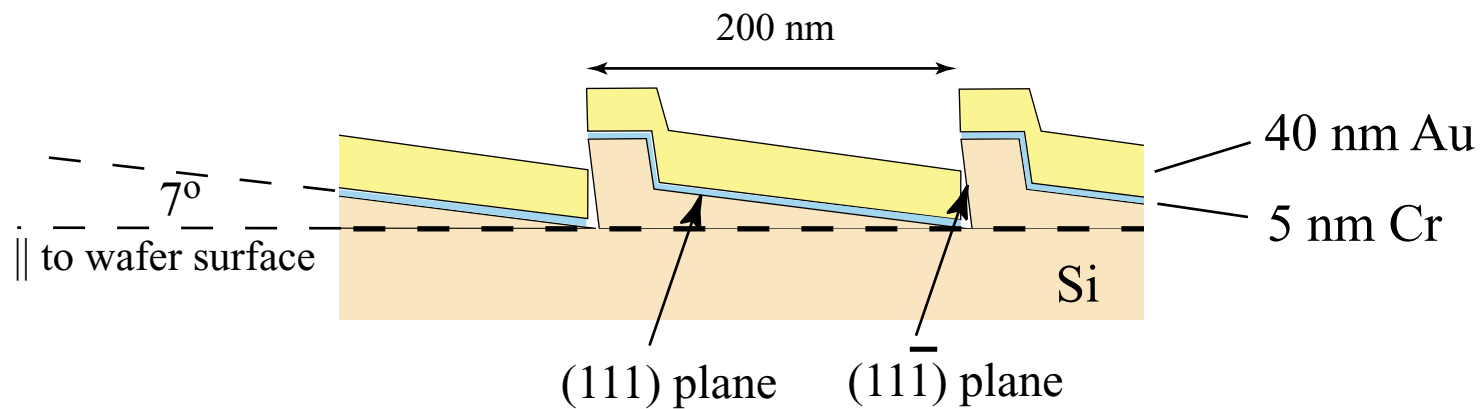
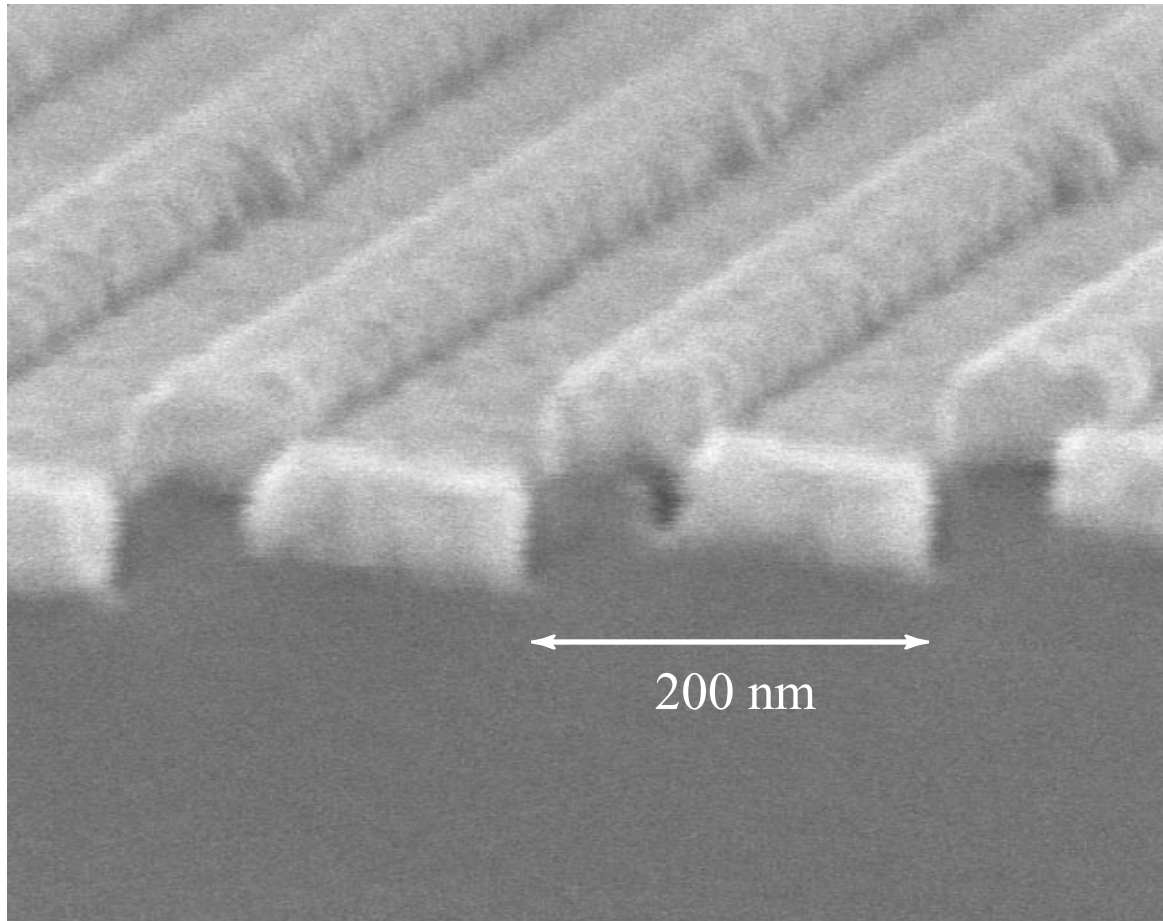
5. Remove nitride with HF.



3. RCA clean.

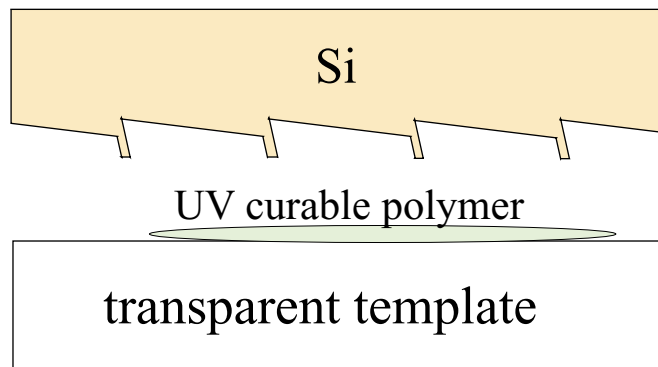


SEM of Blazed Off-Plane Grating

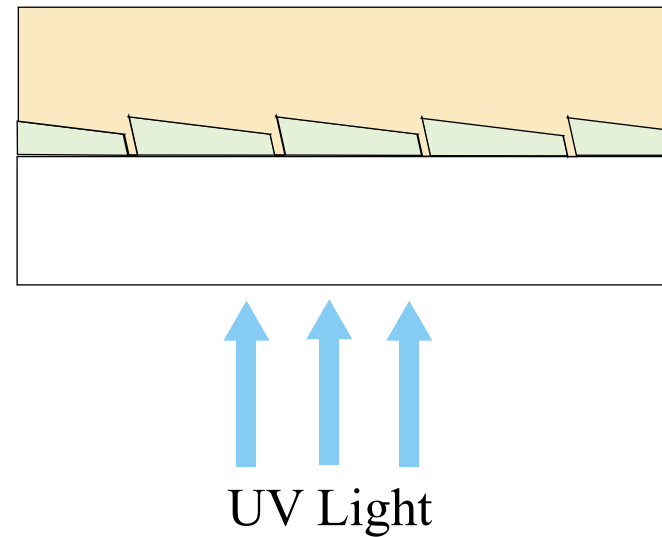


Grating Replication From Silicon Master

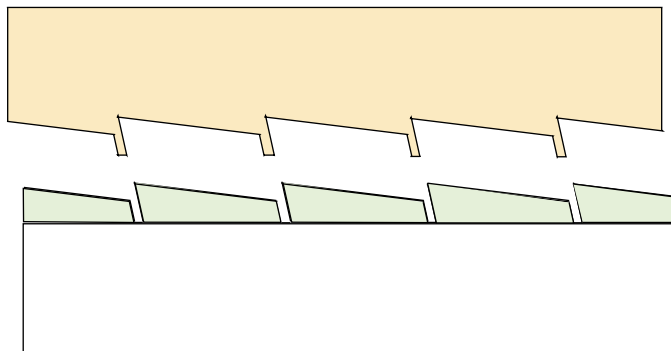
1. Add polymer to the surface



2. Close the gap, cure polymer with UV light



3. Remove mold

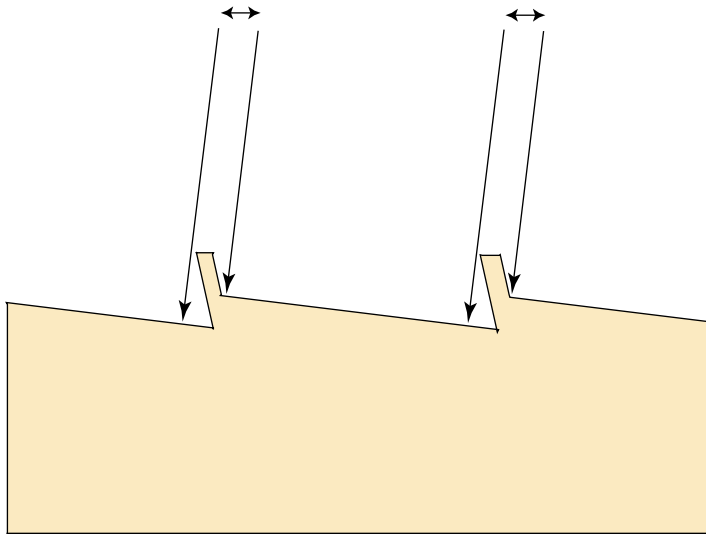


- cheaper
- boosts efficiency?

Off-Plane Grating Effective Area

Si master:

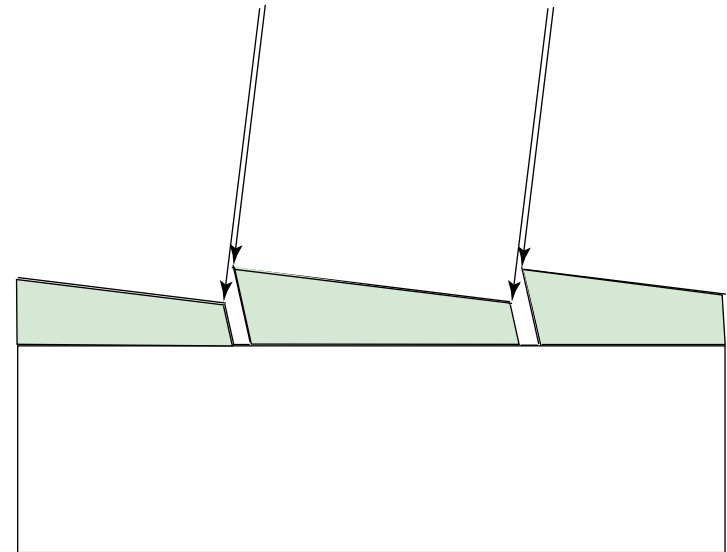
- area covered by "nubs" lost



"nubs" can be removed with additional processing steps

Replica:

- area loss negligible



quality of replicated surfaces?

Summary of Milestones

Double-sided etch process for alignment structures (microcombs)

- higher yield
- larger wafers, longer combs
- higher accuracy

Nanoruler enables large area gratings (> 300 mm)

- 3 nm phase control
- sub-nm linewidth control
- future variable period capability ("chirp")

Super smooth blazed off-plane gratings from miscut silicon wafers are feasible